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EFS ID:	5234832				
Application Number:	10575758				
International Application Number:					
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Title of Invention:	Printed Circuit Board Design Instruction Support Method and Device				
First Named Inventor/Applicant Name:	Hiromichi Inaishi				
Customer Number:	02292				
Filer:	Paul C. Lewis/Candice Lee				
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)		
1		20090428RCE.PDF	177779	yes	3		
			62fb8fad4c7311bf5f5498ba6745eadc9649 8aod				
	Multipart Description/PDF files in .zip description						
147	Document Description Request for Continued Examination (RCE) Extension of Time		Start	End			
			1	1			
			2		2		
	Fee Worksheet (3	3				

Warnings:

Information:

2	Fee Worksheet (PTO-875)	fee-info.pdf	32018	no	2
	,		a32a69d92bd8a1db6daa8lba45e540ad930 9ea35		_

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